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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/819,187	03/28/2001	Akitsugu Ohyoshi	FUJA 18.539	2892
26304	7590	08/04/2004	EXAMINER	
KATTEN MUCHIN ZAVIS ROSENMAN 575 MADISON AVENUE NEW YORK, NY 10022-2585				LESNIEWSKI, VICTOR D
ART UNIT		PAPER NUMBER		
		2155		

DATE MAILED: 08/04/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

fr 2

Office Action Summary	Application No.	Applicant(s)
	09/819,187	OHYOSHI ET AL.
	Examiner	Art Unit
	Victor Lesniewski	2155

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 28 March 2001.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-25 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date: _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>3/28/2001</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This application has been examined.
2. Claims 1-25 are now pending.

Priority

3. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

4. The IDS filed on 3/28/2001 has been considered.

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 1-25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nishihara (U.S. Patent Number 5,764,637).

7. Nishihara disclosed a system which converts STM frame data to ATM cells. Among other features, the system contains a virtual path identifier discrimination section for making determinations on incoming frames, a plurality of cell buffer queues and memory queues for

storage and transfer of the frames/cells throughout the system, and a writing operation for assembling the ATM cells. Specific claims will be discussed in more detail below.

8. Nishihara has disclosed:

- <Claim 1>

A method for switching communication modes for shifting an exchange from a frame relay exchange to an ATM exchange in a communication system having a plurality of exchanges each accommodating a plurality of terminals and including a frame relay exchange and having a network for connecting the plurality of exchanges to each other in order to transfer data among said plurality of terminals, comprising a first step of selecting any one exchange among said plurality of exchanges (column 2, lines 46-49), a second step of operating said selected exchange as a frame/cell compatible type exchange capable of operating for both of frames and cells (column 2, lines 53-57), and a third step of executing said first step and second step for an exchange which has not yet been selected and repeating said third step to autonomously shift all said exchanges to said ATM exchanges (column 2, lines 46-49, namely “each time slot position”).

Nishihara disclosed a system which converts STM frame data to ATM cells. Although he does not specifically delineate the exact steps as described above, he still discloses all the limitations of claim 1 and the described steps are a clear extension from this disclosure. For example, Nishihara does not speak specifically of shifting each individual frame through the conversion. Instead he speaks of the complete conversion that occurs by way of “each time slot position.” Since Nishihara has disclosed the same limitations only stated a different way, it would have been obvious to one of ordinary skill in the art

at the time of the applicant's invention to delineate the conversion steps in terms of each singular exchange.

- <Claim 2>

A method for switching communication modes as set forth in claim 1, wherein, in said second step, whether an opposing exchange or an opposing terminal of an other party of communication operates for frames or operates for cells is registered on said selected exchange side in advance as office data, and the selected exchange is operated as an exchange operating for frames or an exchange operating for cells according to the office data.

Concerning claims 2 and 3, Nishihara disclosed a virtual path identifier discrimination unit which identifies the incoming frame exchanges and passes them to a cell buffer queue to be processed accordingly. This can be considered an automatic determination by the system. However, a predetermined discrimination was also well known in the art at the time of the applicant's invention. Furthermore, Nishihara points to the use of predetermined exchange information by saying how conventional conversion systems utilize a predetermined restriction on the incoming signal and a predetermined transfer order in the cell buffer. See column 1, lines 42-50. In terms of claim 2, it would have been obvious to one of ordinary skill in the art at the time of the applicant's invention that a previously registered determination could have been made as opposed to the automatic one.

- <Claim 3>

A method for switching communication modes as set forth in claim 1, wherein, in said second step, whether an opposing exchange or an opposing terminal of another party of communication operates for frames or operates for cells is automatically identified in said selected exchange (column 2, lines 46-49), and the selected exchange is operated as an exchange operating for frames or an exchange operating for cells according to the result of the identification (column 2, lines 53-57).

- <Claim 4>

An exchange provided with: an ATM side bus and a frame side bus provided in parallel (figure 3), a frame/cell compatibility function unit inserted in said ATM side bus (figure 3, item 102), and a frame/cell switch unit for alternatively switching between said ATM side bus and frame side bus (figure 3, item 101).

Although Nishihara does not specifically describe the exact conversion units as the above claim, he still discloses all the limitations of claim 4 and the above named items accomplish the same functionality as those units of the claim. For example, a separate compatibility function unit and a separate switch unit are represented in the stated items of figure 3 which accomplish a check for compatibility and a switching between the frame and ATM sides respectively. Furthermore, Nishihara's system still accomplishes the same functionality although it is not explicitly stated as having parallel buses. Since Nishihara has disclosed the same limitations only stated a different way, it would have been obvious to one of ordinary skill in the art at the time of the applicant's invention to

utilize parallel buses and separate functional units in order to accomplish the same functionality.

- <Claim 5>

An exchange as set forth in claim 4, wherein said frame/cell compatibility function unit is comprised of an ATM switch and a frame/cell format converting unit connected to the ATM switch (column 4, lines 39-47).

- <Claim 6>

An exchange as set forth in claim 5, wherein said frame/cell format converting unit is a frame forwarding CLAD circuit (column 4, lines 47-51).

- <Claim 7>

An exchange as set forth in claim 4, wherein said frame/cell switch unit switches alternatively between said ATM side bus and frame side bus in accordance with an instruction from the outside (column 3, lines 28-37).

- <Claim 8>

An exchange as set forth in claim 7, wherein said instruction is given according to office data registered in advance.

See claim 2 for a description of the previously registered determination.

- <Claim 9>

An exchange as set forth in claim 7, further provided with: a frame/cell detection unit for detecting whether a signal input from the outside is comprised of frames or cells (column 2, lines 46-49), said instruction being created in accordance with the result of detection by this frame/cell detection unit (column 2, lines 53-57).

- <Claim 10>

An exchange as set forth in claim 9, further provided with: a pair of said frame/cell switch units formed at the two end portions of said ATM side bus and frame side bus provided in parallel (column 3, lines 15-20) and a switch control unit for receiving as input the result of detection by said frame/cell detection unit and controlling said pair of interlocked frame/cell switch units (figure 4, item 206).

To clarify, although Nishihara does not describe specifically a pair of switch units, the above cited queues allow the system to accomplish the same type of switching tasks.

- <Claim 11>

An exchange as set forth in claim 9, wherein said frame/cell detection unit is comprised of a cell synchronization circuit (column 4, lines 58-65).

- <Claim 12>

An exchange as set forth in claim 9, wherein said frame/cell detection unit has an identifying means for monitoring for cell synchronization during a first time t1 and automatically identifying that an opposing exchange or an opposing terminal of another party of communication operates for frames when detecting that cell synchronization cannot be established during the time t1 (figure 3, item 102).

- <Claim 13>

An exchange as set forth in claim 9, wherein said frame/cell detection unit has an identifying means for monitoring for cell synchronization during a first certain time t1 and automatically identifying that an opposing exchange or an opposing terminal of

another party of communication operates for cells when detecting that cell synchronization can be continuously established during the time t1 (figure 3, item 102).

- <Claim 14>

An exchange as set forth in claim 9, wherein, when said frame/cell detection unit detects that a signal input from the outside is comprised of frames, said frame/cell switch unit switches to said frame side bus and passes the frames through the frame side bus while maintaining the signal format.

Nishihara disclosed a virtual path identifier discrimination unit which identifies the incoming frame exchanges and processes them accordingly. He does not specifically mention that the same signal format may be maintained. However, maintaining the signal format in a situation such as this was well known in the art at the time of the applicant's invention. In a converting system of this type, if a conversion is not to be made the system must either drop the signal or maintain it in its original format. Thus, it would have been obvious to one of ordinary skill in the art at the time of the applicant's invention that the signal format could be maintained after the detection step.

- <Claim 15>

An exchange as set forth in claim 9, wherein, when said frame/cell detection unit detects that a signal input from the outside is comprised of cells, said frame/cell switch unit switches to said ATM side bus, inputs the cells to said frame/cell compatibility function unit, processes the cells and converts them to the frame signal format, then passes the same through the ATM side bus (column 4, lines 52-57).

- <Claim 16>

An exchange as set forth in claim 14: wherein said frame/cell detection unit has an identifying means for subsequently monitoring for cell synchronization during a second certain time t2 while passing said frames through said frame side bus while maintaining the signal format and automatically identifying that an opposing exchange or opposing terminal of another party of communication has changed to one operating for cells when detecting that cell synchronization could be established during the time t2, and said frame/cell switch unit switches the bus to said ATM side bus, inputs the cells to said frame/cell compatibility function unit, processes the cells and converts them to the frame signal format, and then passes the same through the ATM side bus.

Concerning claims 16 and 17, Nishihara disclosed a system which monitors cell synchronization and operates on frames/cells determined ready for conversion. See column 4, lines 39-65. He does not specifically mention the distinctions of separate times t2, t3, etc. However, these specific time intervals are not patentably distinct from the fact that the cell synchronization is monitored throughout as it is clear that the monitoring can take place at any such chosen time. Thus, it would have been obvious to one of ordinary skill in the art at the time of the applicant's invention to include monitoring for cell synchronization at specific times throughout the process.

- <Claim 17>

An exchange as set forth in claim 15, wherein said frame/cell detection unit has an identifying means for subsequently monitoring for cell synchronization during a third certain time t3 while passing said cells through said ATM side bus and automatically

identifying that an opposing exchange or opposing terminal of another party of communication has changed to one operating for frames when detecting that cell synchronization cannot be established during the time t3, and said frame/cell switch unit switches the bus to said frame side bus and passes the frames through the frame side bus while maintaining the signal format.

See discussion under claim 16 for the monitoring at a certain time and the discussion under claim 14 for maintaining signal format.

- <Claim 18>

An exchange as set forth in claim 12 or 13, wherein when communicating with said opposing exchange or opposing terminal via high speed digital dedicated lines, said monitoring is carried out in units of B channels x N (N is 1, 2, 3, ...) (column 5, lines 24-30).

- <Claim 19>

An exchange as set forth in claim 12 or 13, wherein when communicating with said opposing exchange or opposing terminal via an ISDN backup channel at the time of a line fault, said monitoring is carried out with respect to the related communication in units of B channels x N (N is 1, 2, 3, ...) during the period from completion of the connection to the ISDN backup channel to restoration of the channel due to the end of the line fault (column 1, lines 24-32 and column 5, lines 24-30).

- <Claim 20>

An exchange as set forth in claim 7, further provided with a system console, the switch state to said ATM side bus or to frame side bus by said frame/cell switch unit being displayed on the system console.

Although Nishihara did not explicitly state the use of a display in his system, system consoles were well known in the art at the time of the applicant's invention in order to monitor a variety of pieces of a conversion system. Thus it would have been obvious to one of ordinary skill in the art at the time of the applicant's invention to include a system console.

- <Claim 21>

An exchange as set forth in claim 12 or 13, wherein said first certain time t1 is registered in units of B channels x N (N is 1, 2, 3, ...) or in units of channels by a command from the outside (column 5, lines 24-30).

- <Claim 22>

An exchange as set forth in claim 16, wherein said second certain time t2 is registered in units of B channels x N (N is 1, 2, 3, ...) or in units of channels by a command from the outside (column 5, lines 24-30).

In addition, see discussion under claim 16 for the monitoring at a certain time.

- <Claim 23>

An exchange as set forth in claim 17, wherein said third certain time t3 is registered in units of B channels x N (N is 1, 2, 3, ...) or in units of channels by a command from the outside (column 5, lines 24-30).

In addition, see discussion under claim 16 for the monitoring at a certain time.

- <Claim 24>

An exchange as set forth in claim 7, wherein whether an opposing exchange or an opposing terminal of another party of communication operates for frames or operates for cells as office data is registered in units of B channels x N (N is 1, 2, 3 ...) or in units of channels (column 5, lines 24-30).

- <Claim 25>

An exchange as set forth in claim 9, wherein the fact that the identification of whether the opposing exchange or opposing terminal of the other party of communication operates for frames or operates for cells should be automatically identified by said frame/cell detection unit is registered as the office data in units of B channels x N (N is 1, 2, 3, ...) or in units of channels (column 5, lines 24-30).

Since Nishihara has disclosed all of the above limitations, claims 1-25 are rejected.

Conclusion

9. The prior art made of record and not relied upon is considered pertinent to the applicant's disclosure.

- Sugita (U.S. Patent Number 5,440,552) disclosed an ATM cell assembling/disassembling system.
- Shobatake et al. (U.S. Patent Number 5,557,609) disclosed a switching apparatus for ATM that includes frame/cell synchronization processes.

- Haddock (U.S. Patent Number 5,560,038) disclosed a translation engine for translating frames of data from one frame format to another.
- Keshav et al. (U.S. Patent Number 5,623,605) disclosed methods to enable connectionless-oriented programs to communicate with connection-oriented routines.
- Miura et al. (U.S. Patent Number 5,859,848) disclosed an ATM packet conversion system.
- Kerns et al. (U.S. Patent Number 6,430,185) disclosed an apparatus for bidirectional data transfer that includes an ATM adapter.
- Lee (U.S. Patent Number 6,529,510) disclosed an ATM switching apparatus.

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Victor Lesniewski whose telephone number is 703-308-6165. The examiner can normally be reached on Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hosain Alam can be reached on 703-308-6662. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

VZ
Victor Lesniewski
Patent Examiner
Group Art Unit 2155


Patrice Winder
PATRICE WINDER
PRIMARY EXAMINER